

Figure 1. Eye Pattern

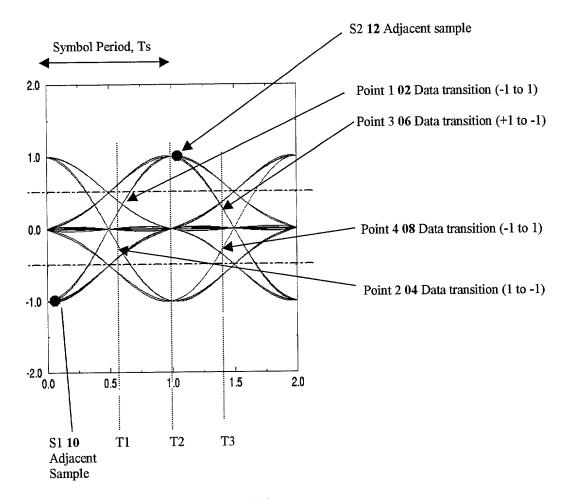
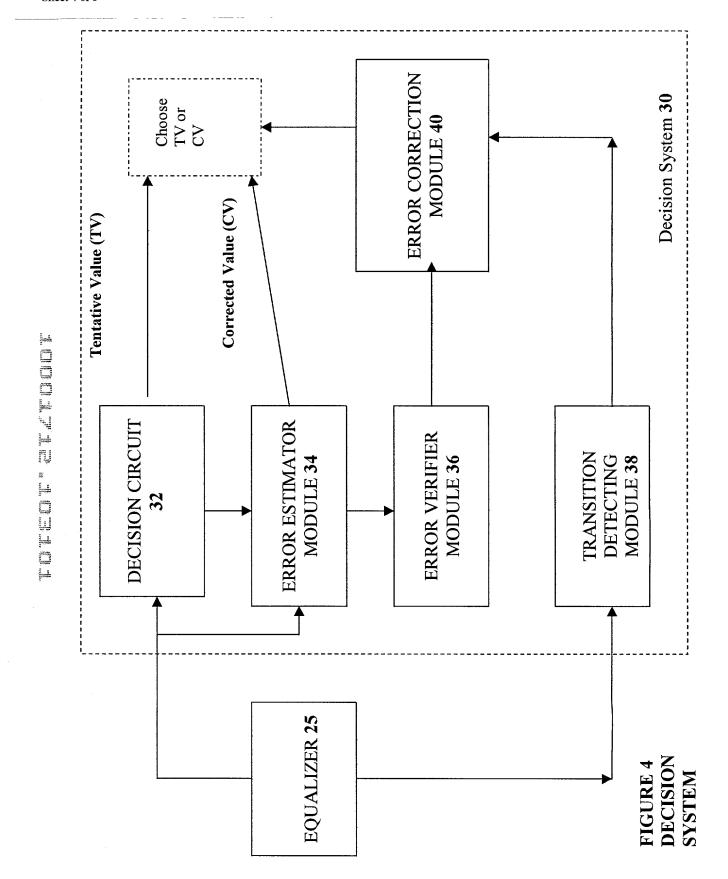


Figure 2. Error Events in Eye Diagram

Receiver 20 Decision 30 Decision 30 Phase Detector 26 Clock (Sampler for A/D)

Figure 3. Block Diagram of the Entire Receiver



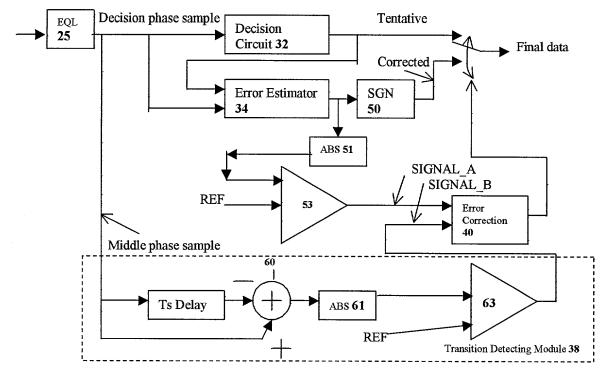


Figure 5. Block Diagram of Error Corrector